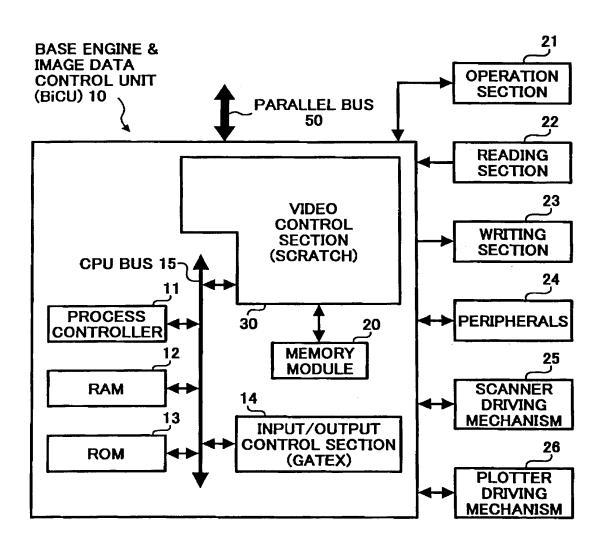
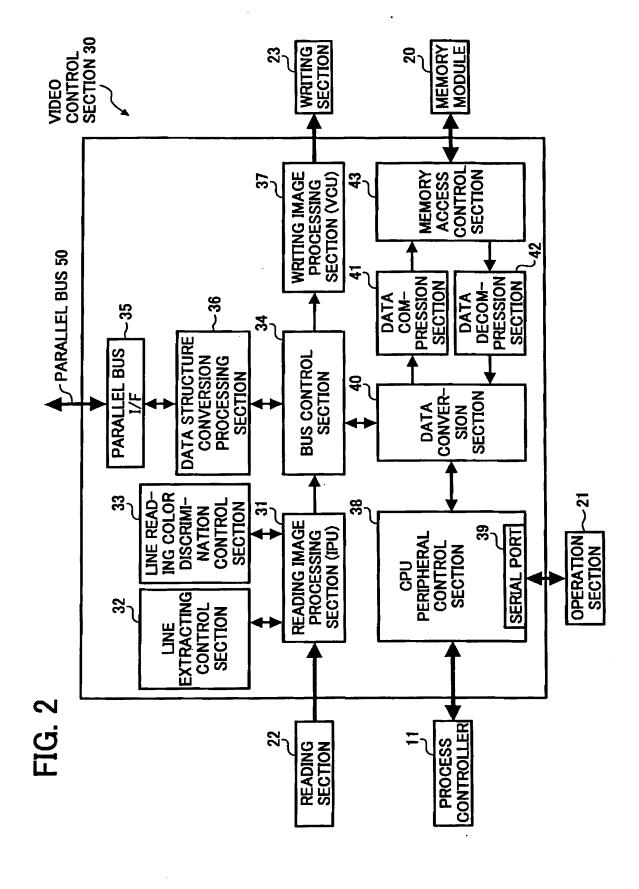
FIG. 1





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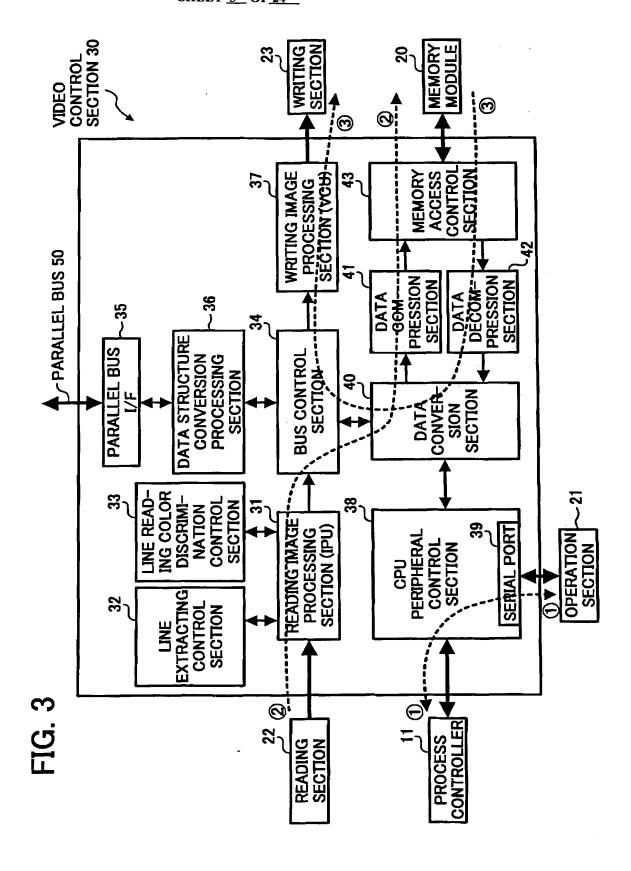
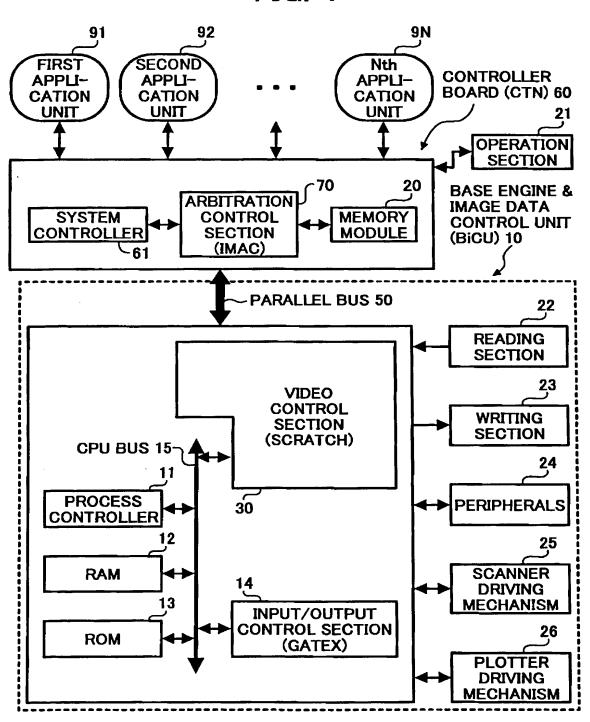
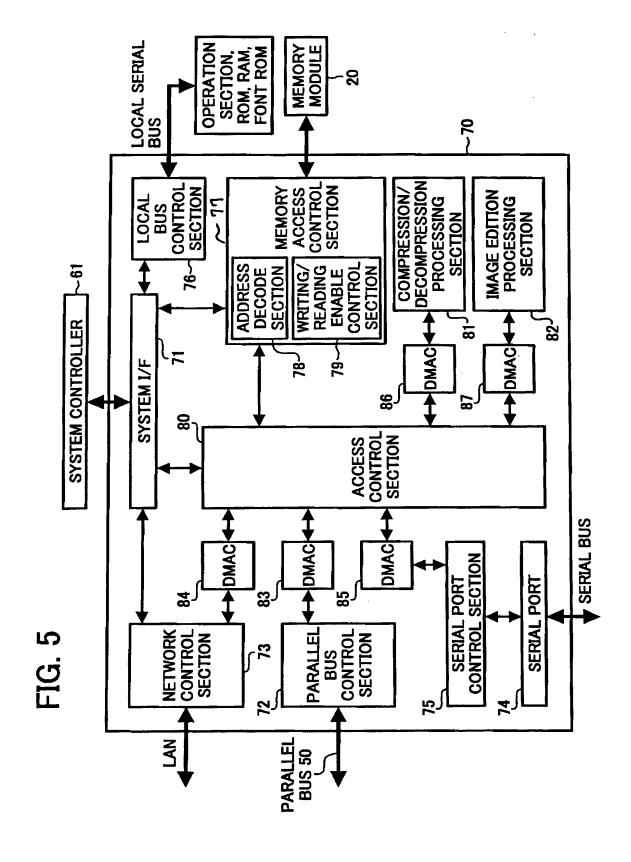


FIG. 4



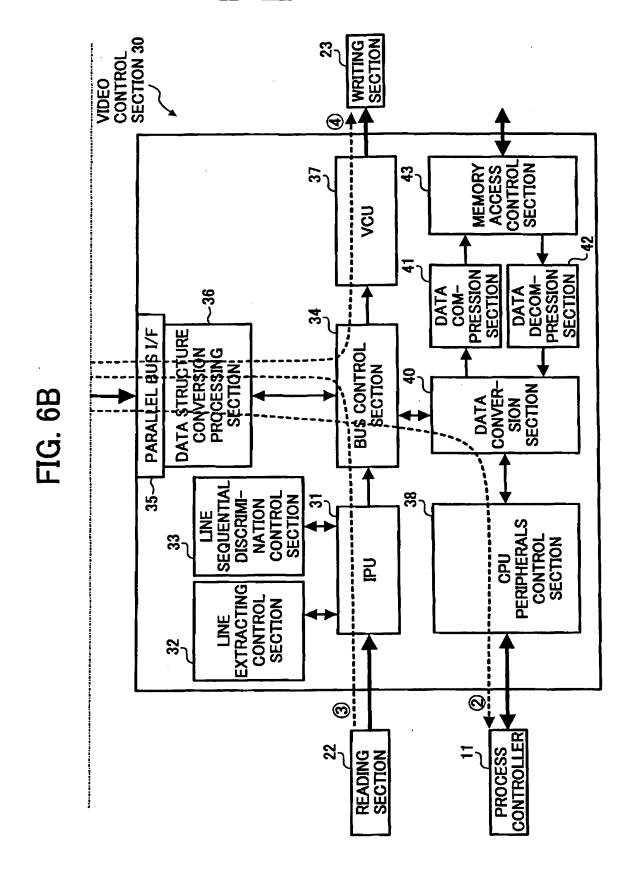
OBLON, SPIVAK, ET AL DOCKET #: 240759US-2 INV: Yoshiyuki NAMIZUKA SHEET <u>5</u> OF <u>24</u>



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OPERATION SECTION **6B** FIG. 6A FIG.  $\Theta$ **CTN 60** MEMORY MODULE **APPLICATION** LIND **(19)** IMAGE MEMORY ACCESS CONTROLLER FIG. 6A APPLICATION FIND PARALLEL BUS 50 **APPLICATION** 70, FIND 0 SYSTEM **APPLICATION** 61 LIND

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**OPERATION** 

**SECTION** 

FIG. 7

READING SECTION

BICU

WRITING SECTION

21

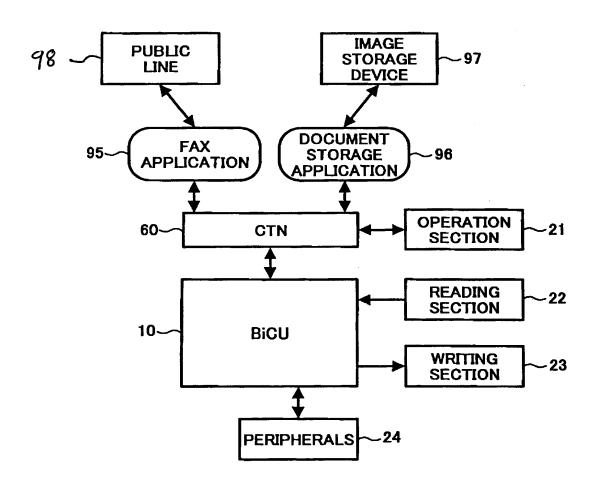
21

24

**PERIPHERALS** 

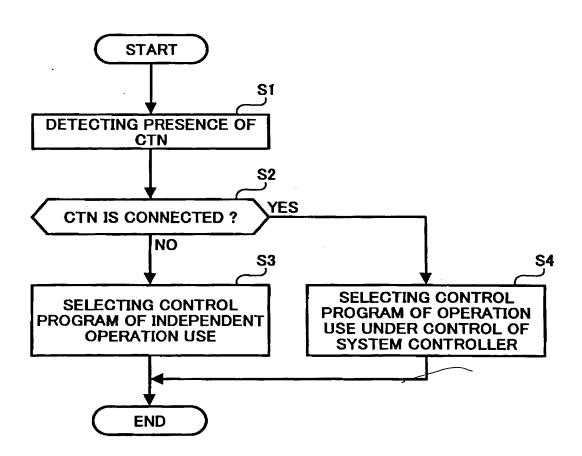
FIG. 8 94 LAN LAN CONNECTION APPLICATION 60 **OPERATION CTN SECTION READING** -22 **SECTION BiCU** WRITING -23 **SECTION** PERIPHERALS

FIG. 9

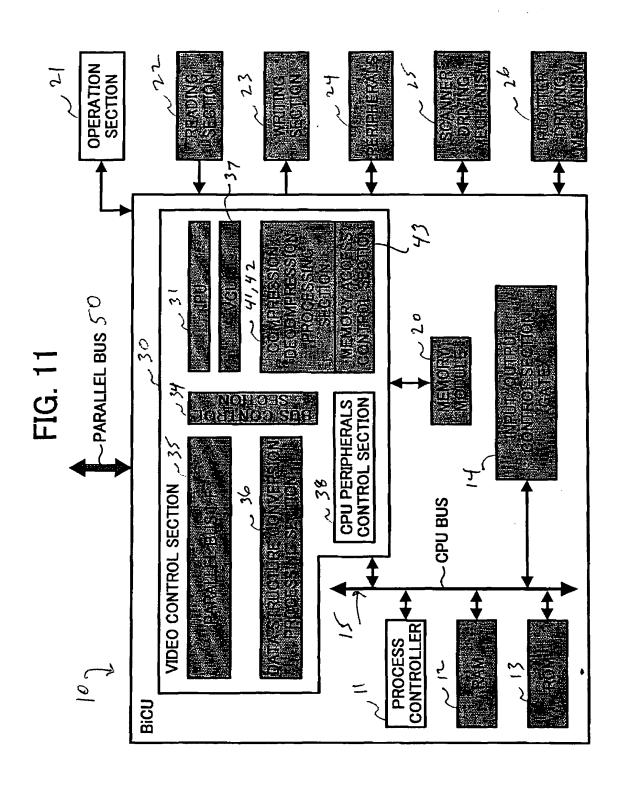


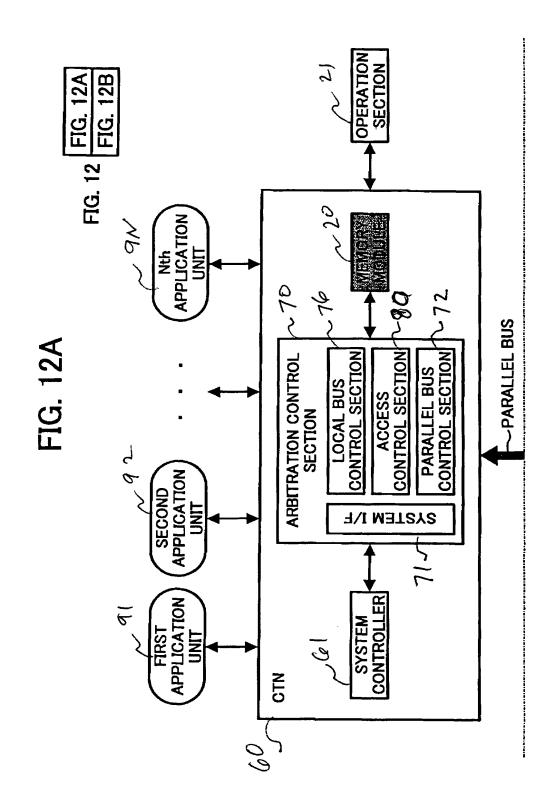
OBLON, SPIVAK, ET AL DOCKET #: 240759US-2 INV: Yoshiyuki NAMIZUKA SHEET <u>10</u> OF <u>24</u>

**FIG.10** 



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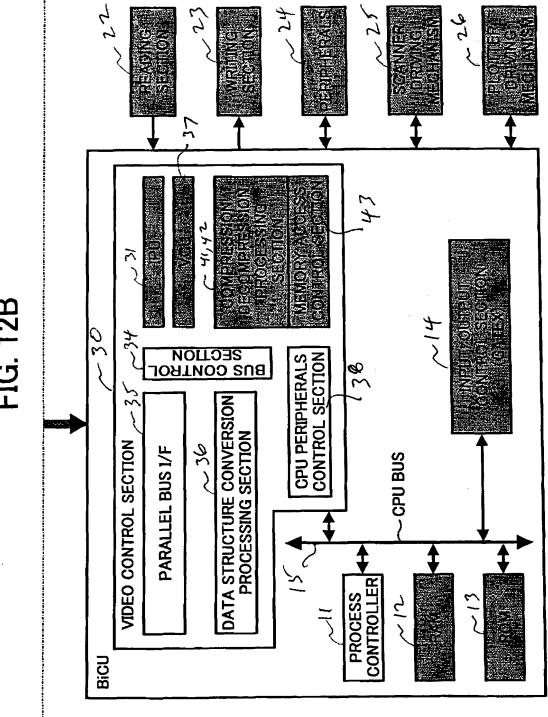
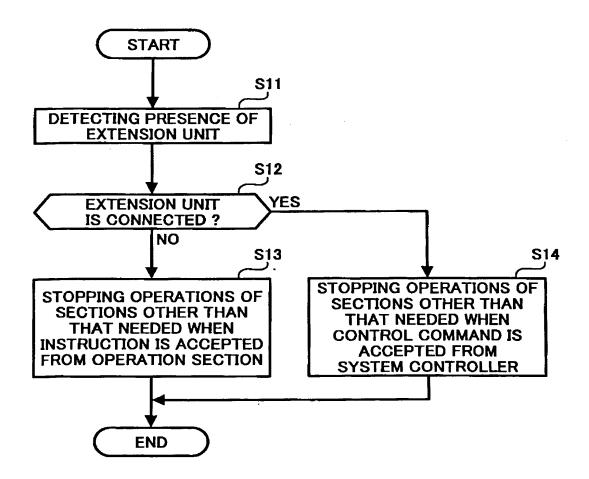


FIG. 12B

OBLON, SPIVAK, ET AL DOCKET #: 240759US-2 INV: Yoshiyuki NAMIZUKA SHEET <u>14</u> OF <u>24</u>

**FIG.13** 



OPERATION SECTION FIG. 14 FIG. 14B (a) (b) MODULE MEMORY - PARALLEL BUS 50 **APPLICATION** LIND IMAGE MEMORY ACCESS CONTROLLER FIG. 14A APPLICATION (G) LIND **APPLICATION** LNS 5 SYSTEM CONTROLLER **APPLICATION** FIND

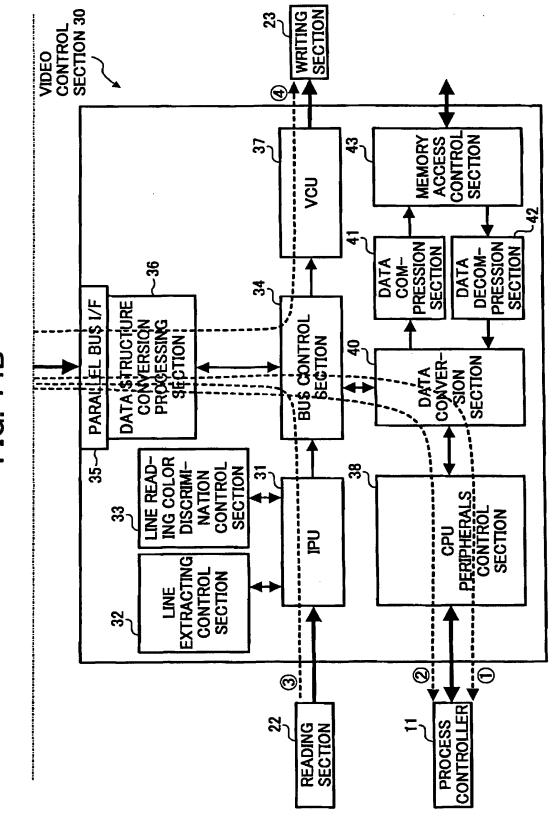


FIG. 14B

OBLON, SPIVAK, ET AL DOCKET #: 240759US-2 INV: Yoshiyuki NAMIZUKA SHEET <u>17</u> OF <u>24</u>

**FIG.15** 

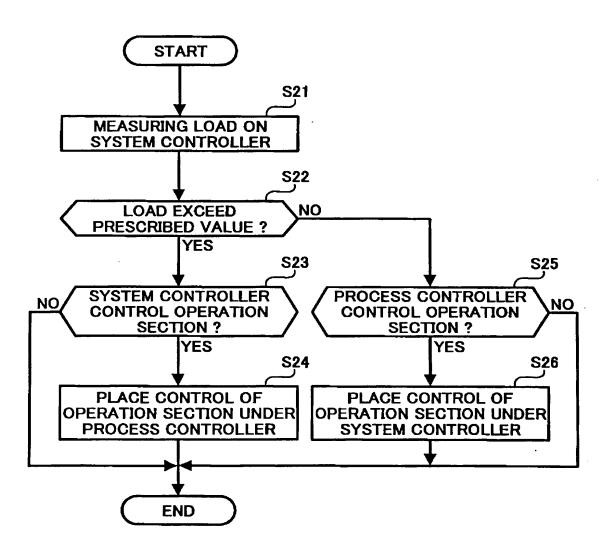
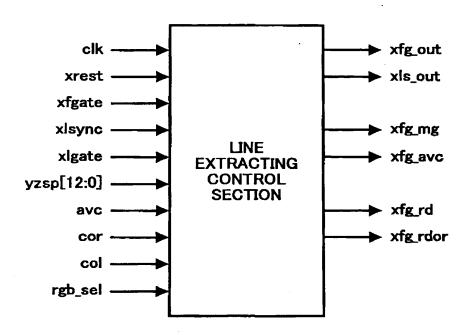
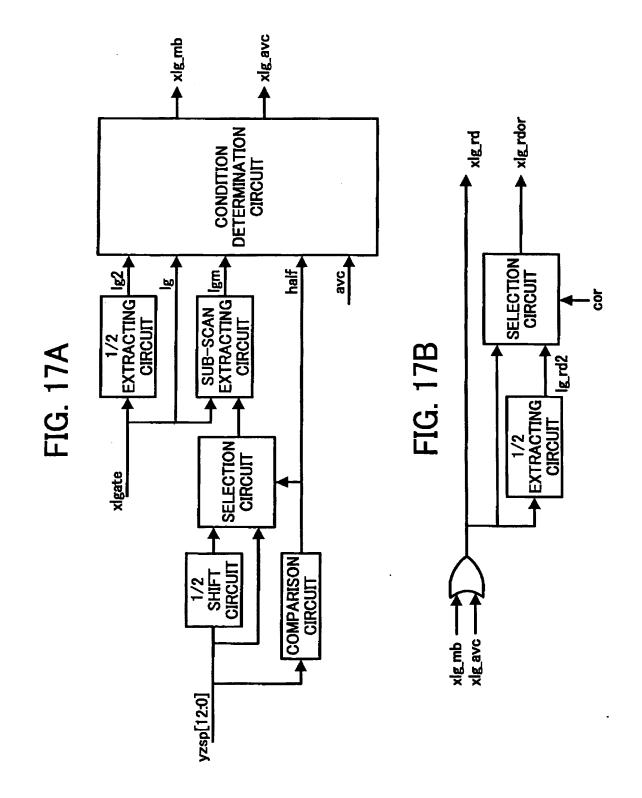


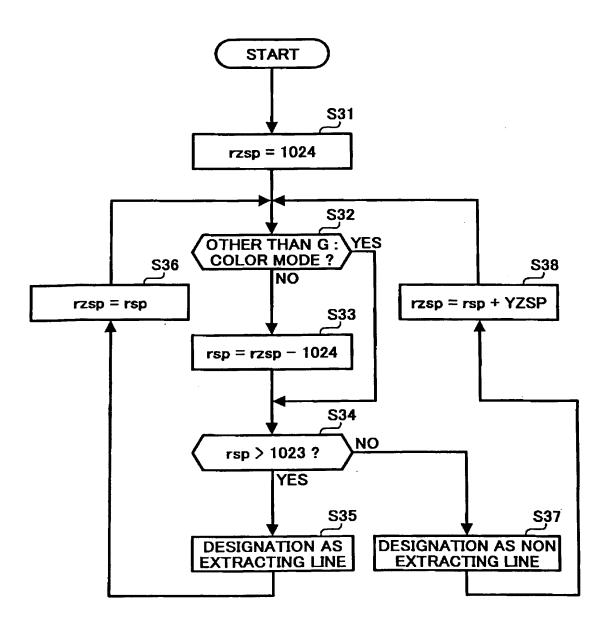
FIG. 16



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**FIG.18** 



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FIG. 19

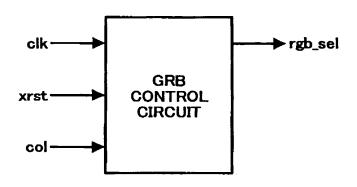
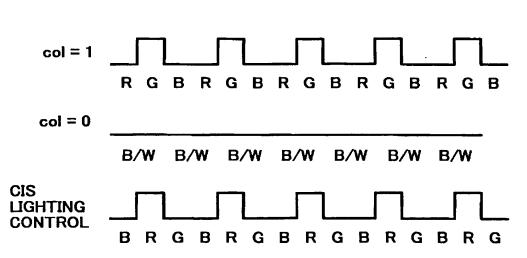
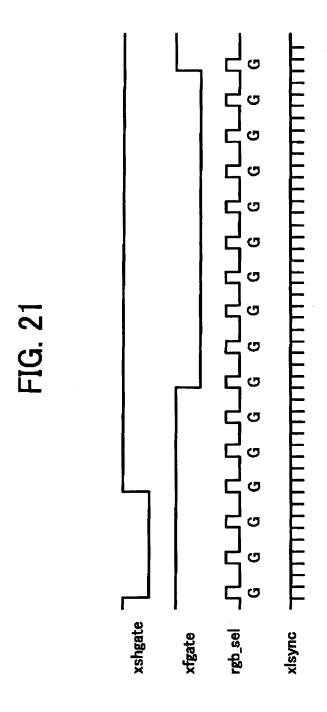
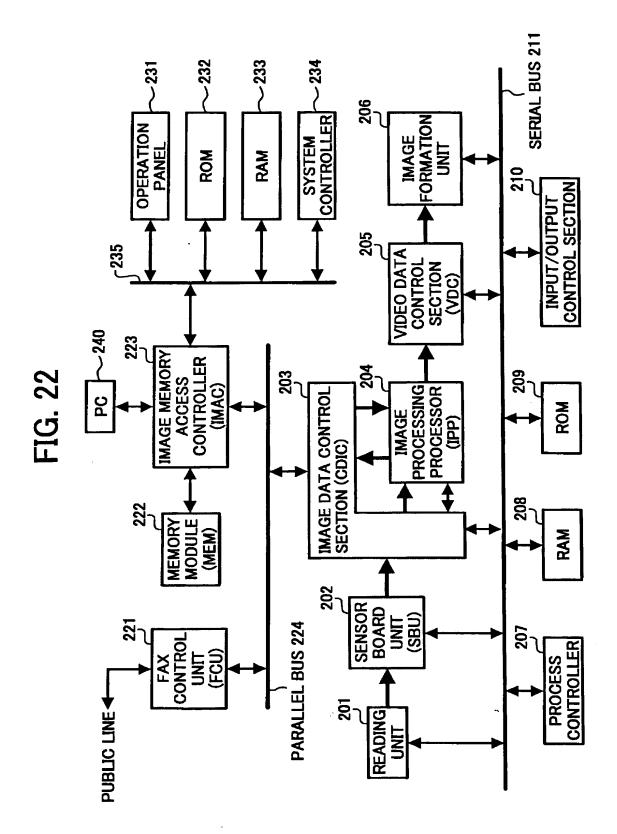


FIG. 20







OBLON, SPIVAK, ET AL DOCKET #: 240759US-2 INV: Yoshiyuki NAMIZUKA SHEET <u>24</u> OF <u>24</u>

## FIG. 23A

<REDUCTION RATE : S%>

MULTI VALUE	EXTRACTION	AVERAGE/COMPARISON	
REDUCTION RATE	-	<b>OVER 50%</b>	LESS THAN 50%
xlg_mb	S%	100%	25%
	(lgm)	(lg)	(lgm)
xlg_avc	100%	S%	50%
	(lg)	(Igm)	(lg2)

## FIG. 23B

**<REDUCTION RATE: S%>** 

BINARY	TOTAL REDUCTION RATE		
OR PROCESS	OFF	ON	
xlg_rd	S%		
xlg_rdor	S% (xlg_rd)	S/2% (lg_rd2)	